

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
INVENTORS NAME: Leonard Forbes
DOCKET NO.: 1303.029US1

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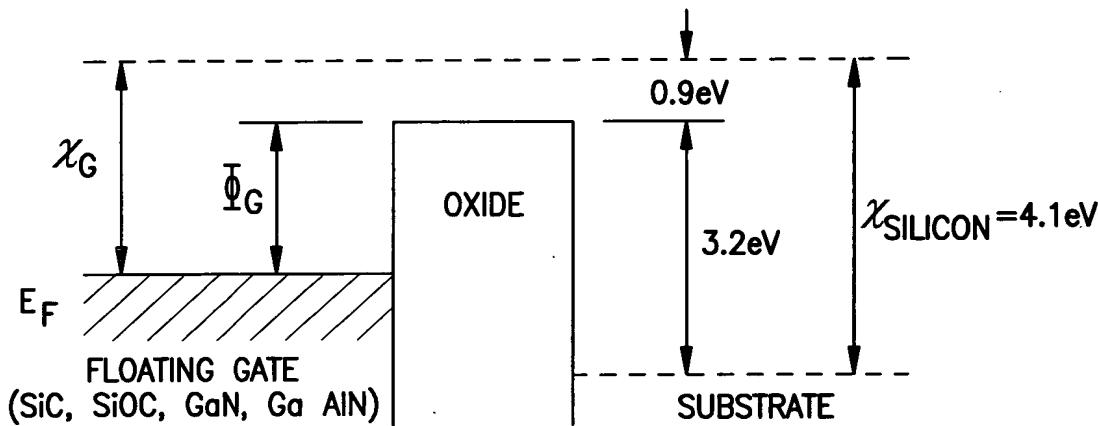


FIG. 1A
(PRIOR ART)

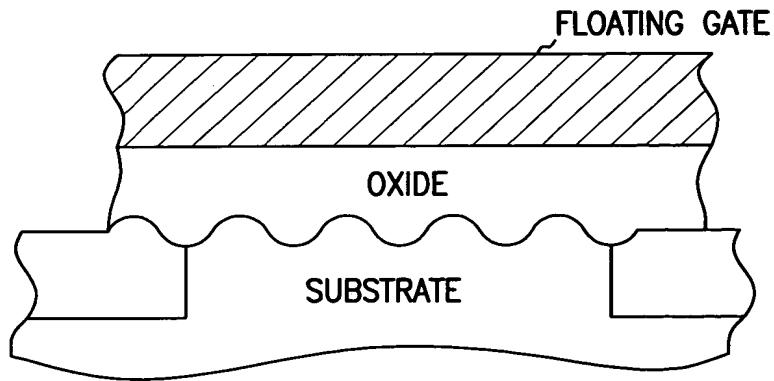


FIG. 1B
(PRIOR ART)

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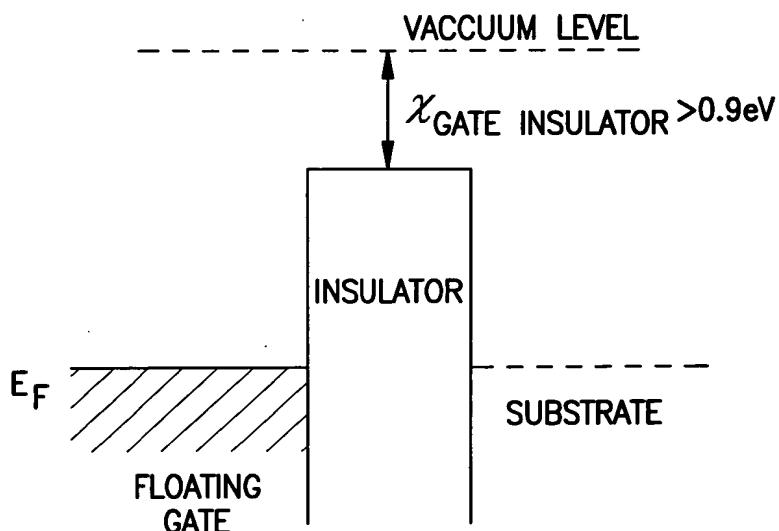


FIG. 1C
(PRIOR ART)

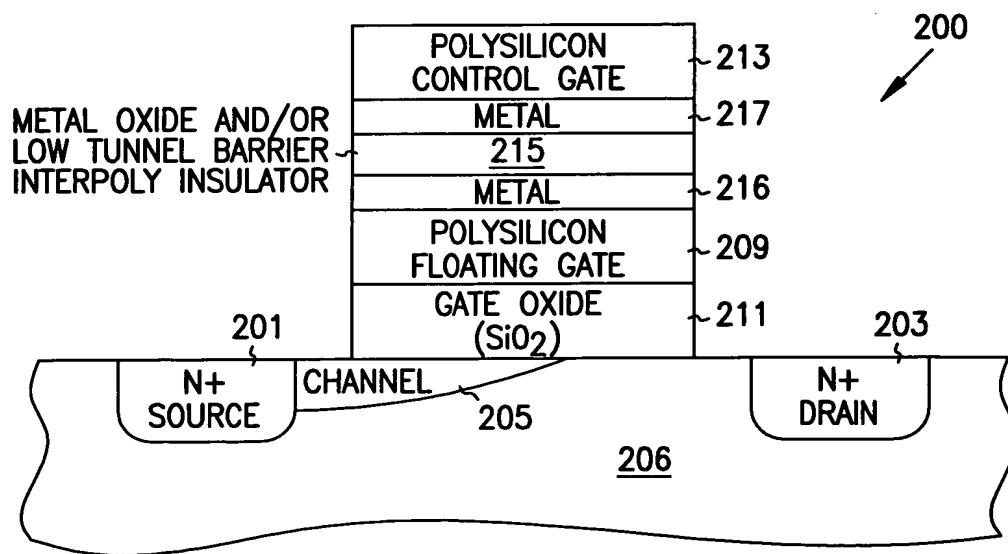


FIG. 2

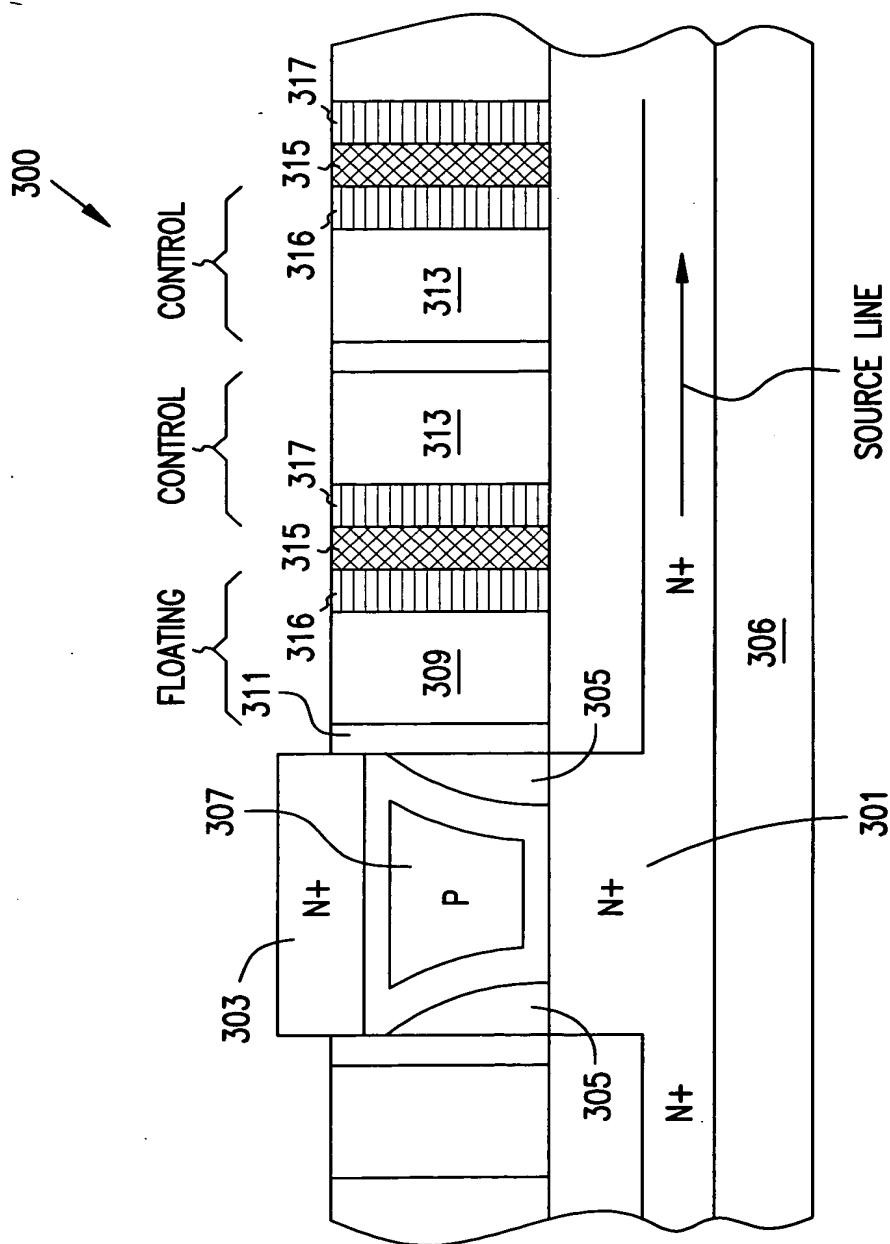


FIG. 3

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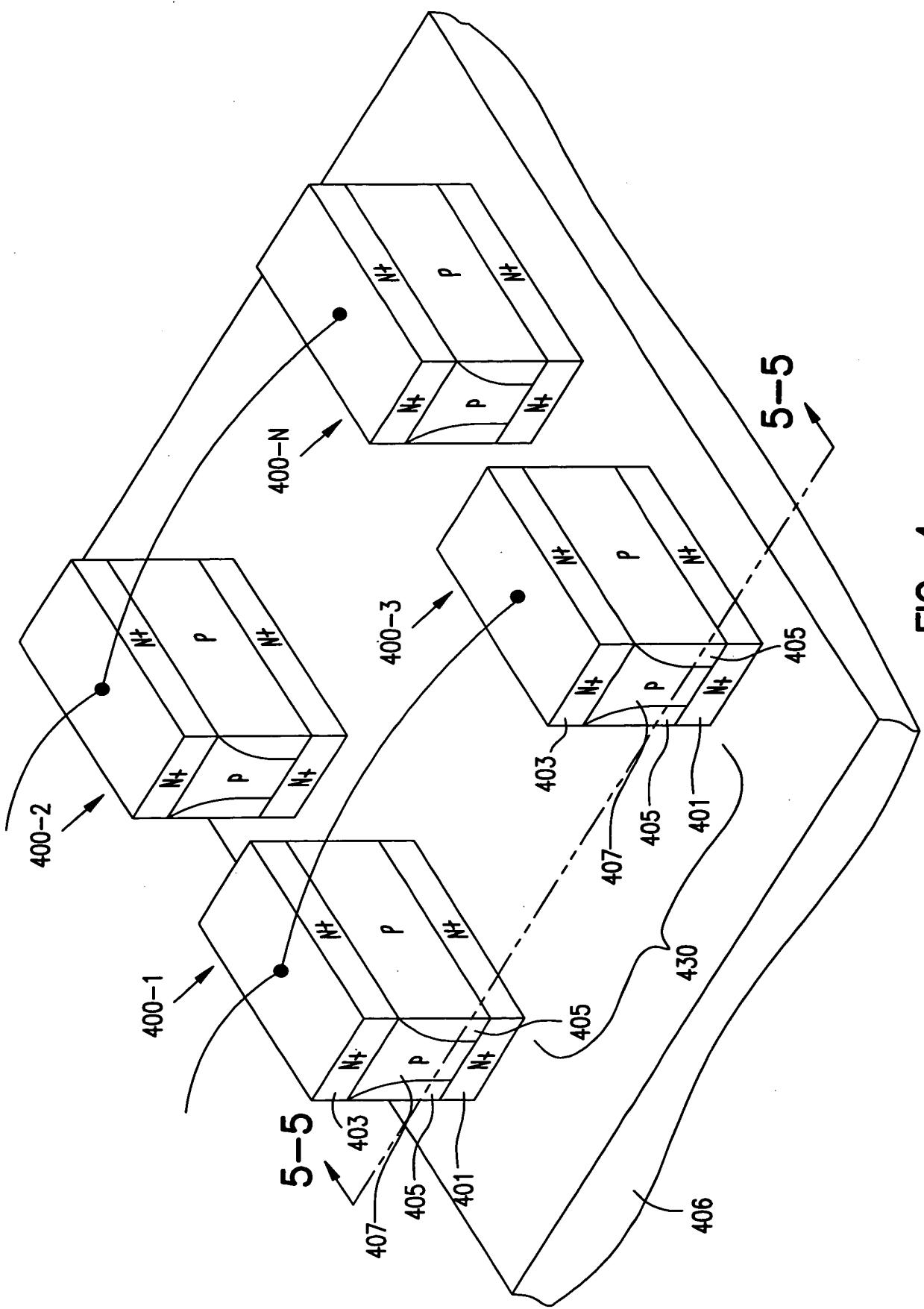


FIG. 4

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL
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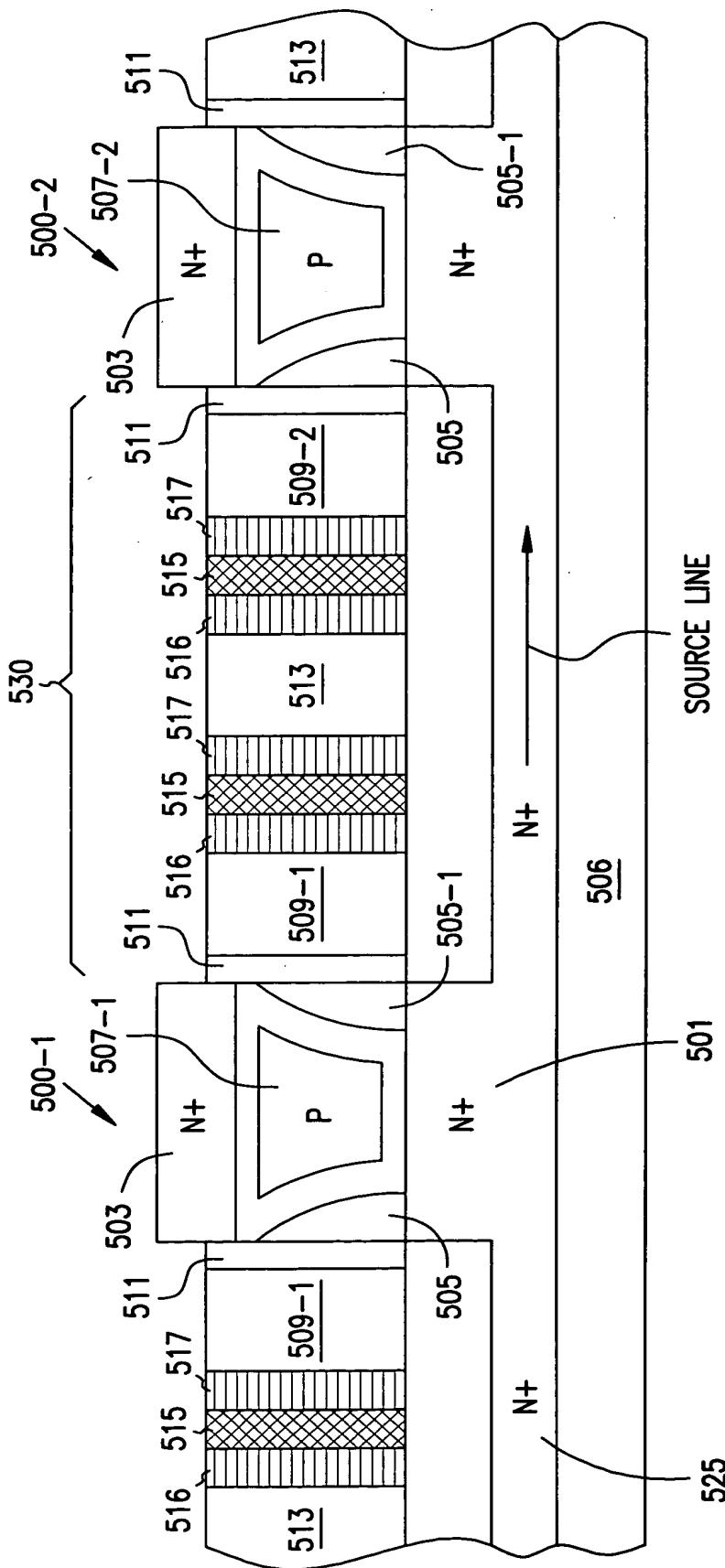


FIG. 5A

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL
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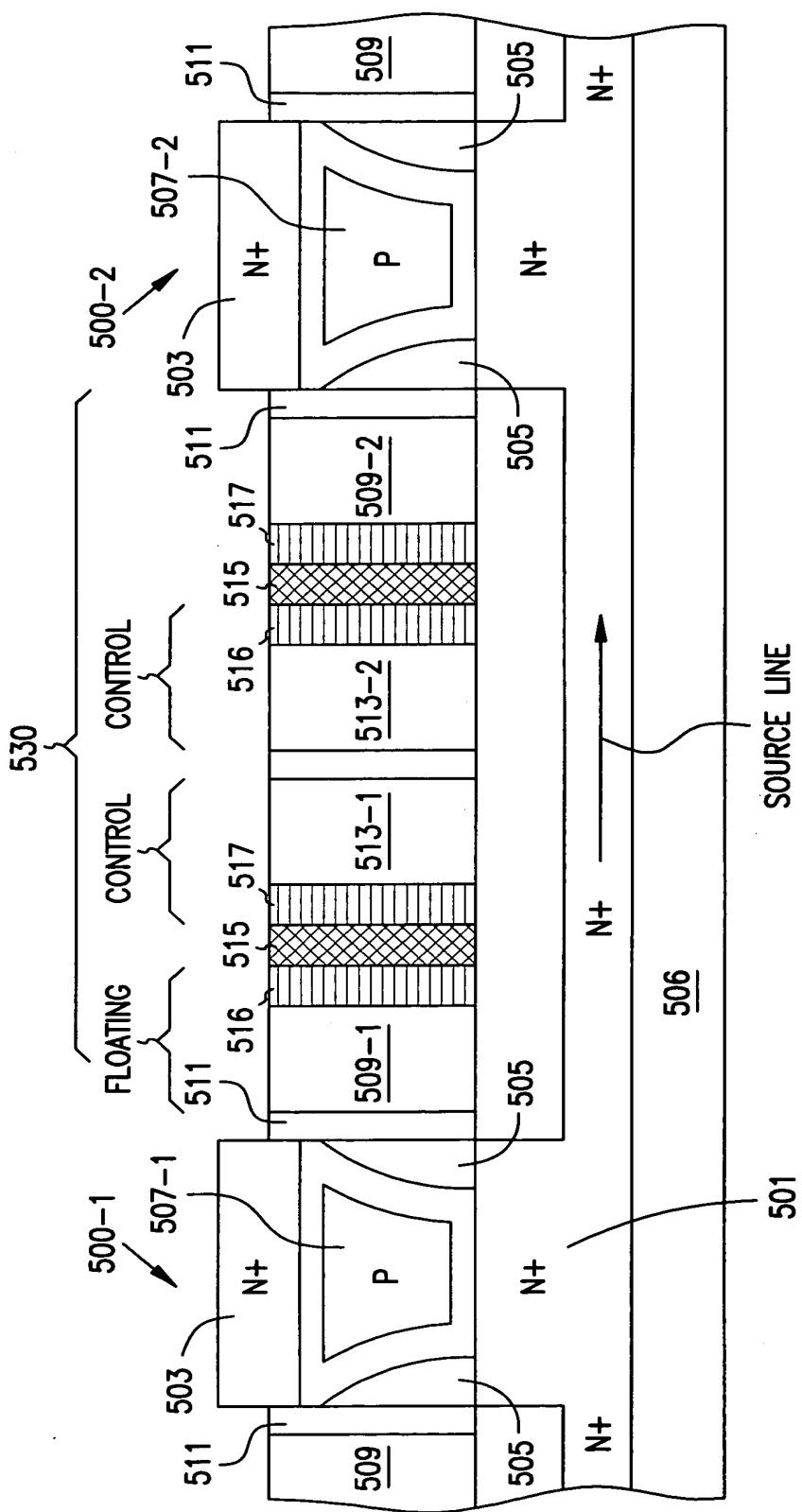


FIG. 5B

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL
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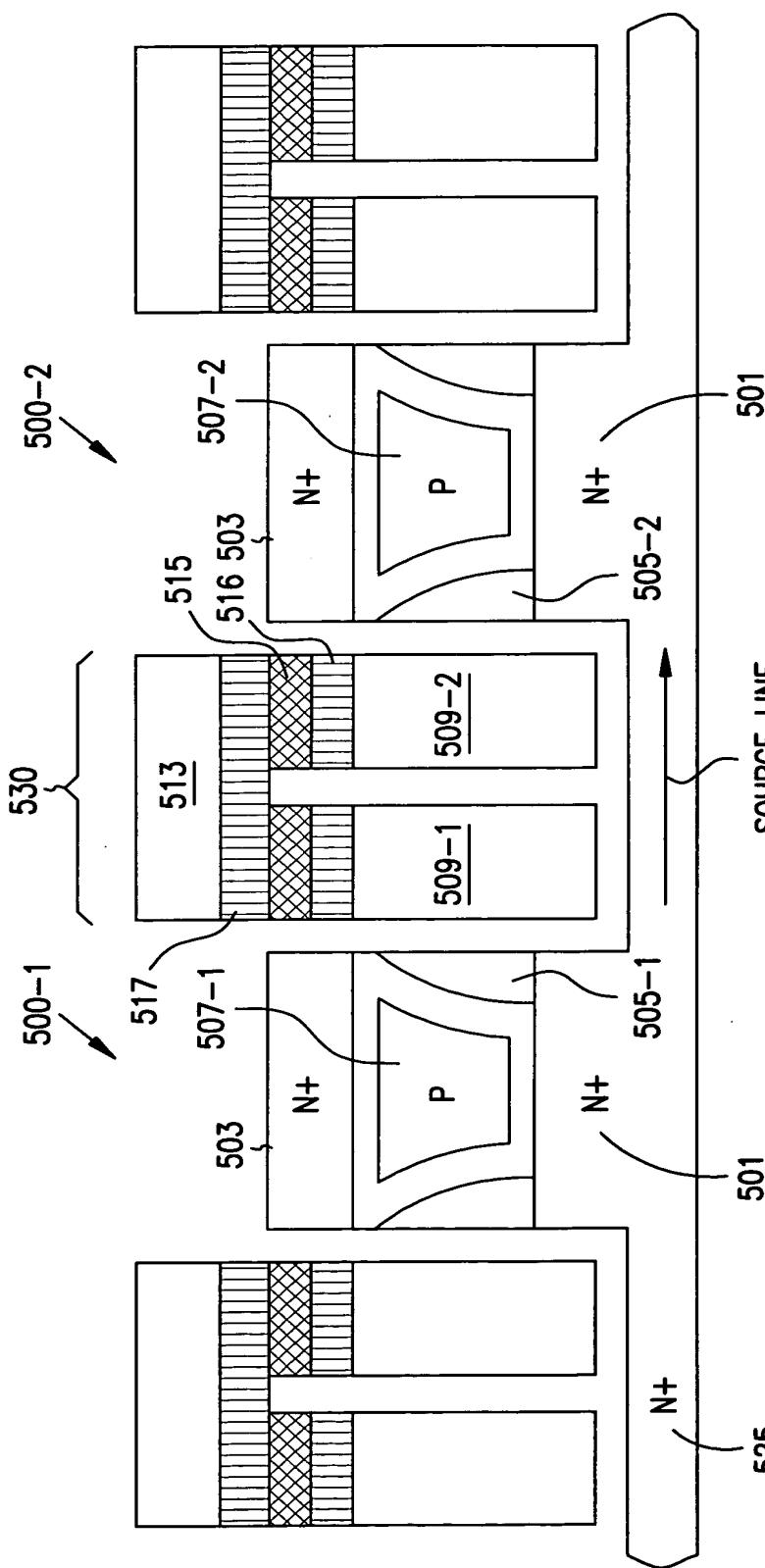


FIG. 5C

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL
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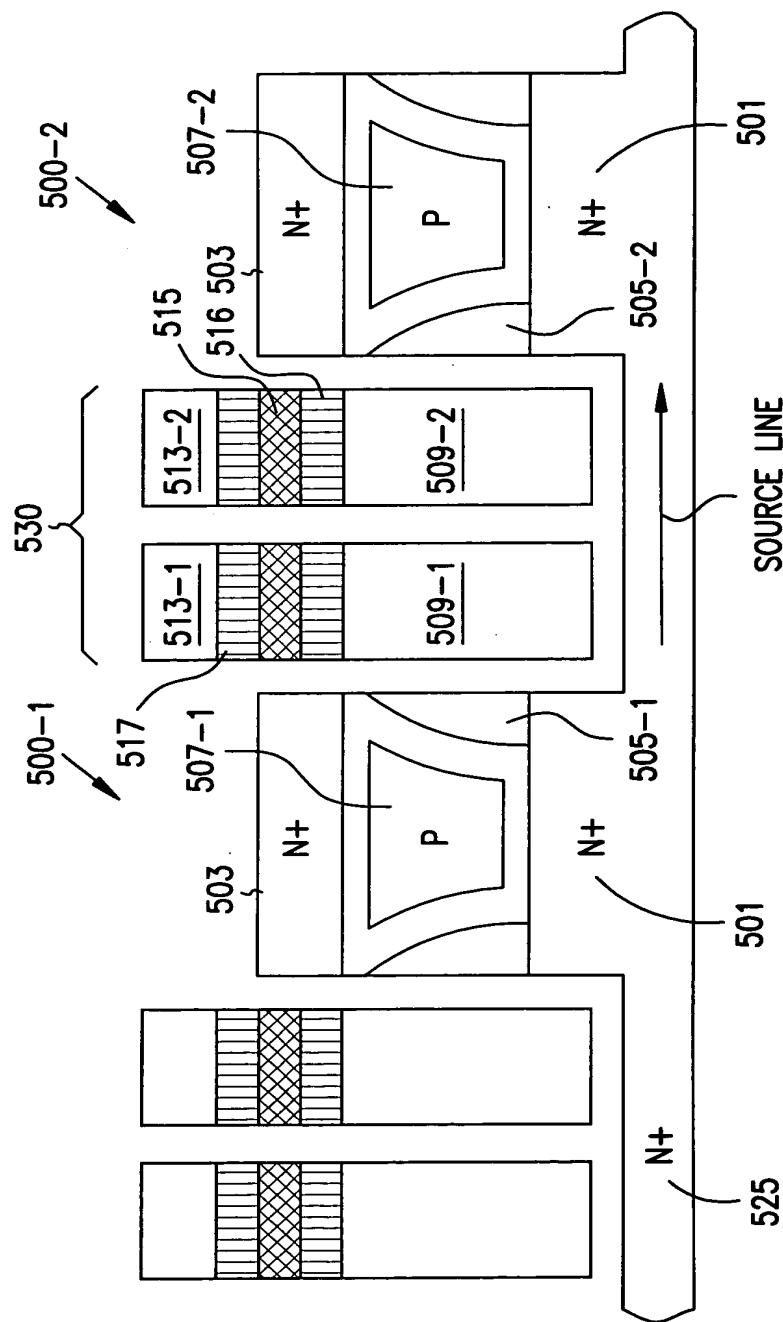


FIG. 5D

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
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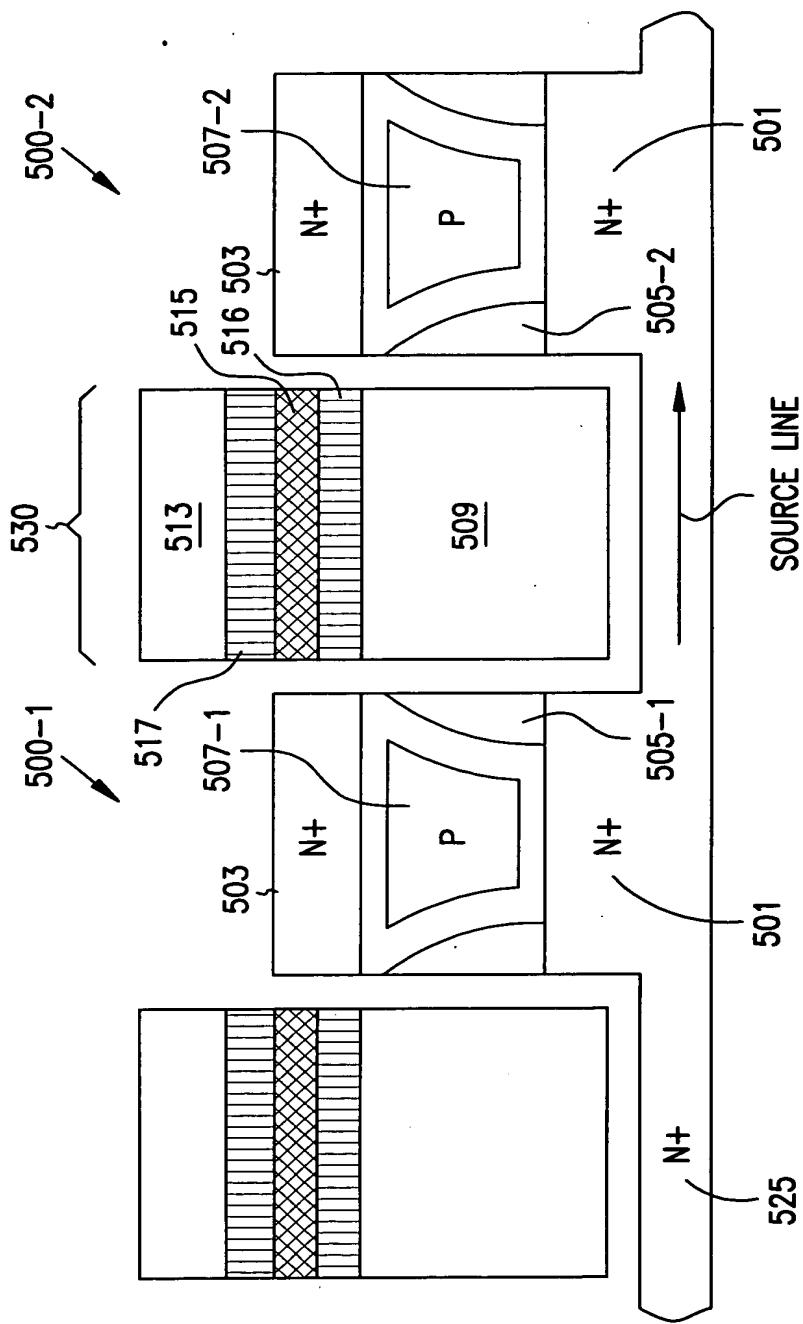


FIG. 5E

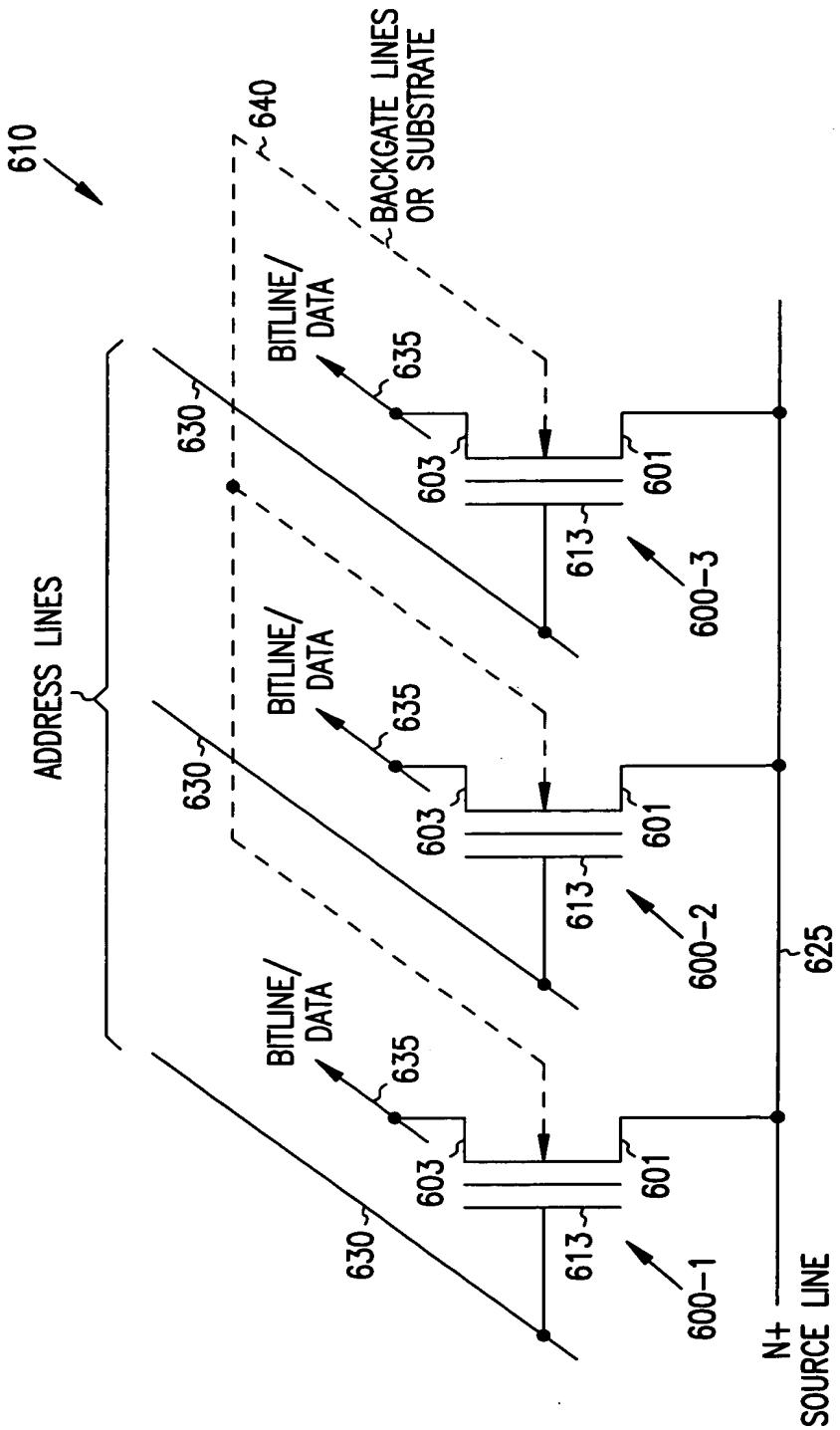


FIG. 6A

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL
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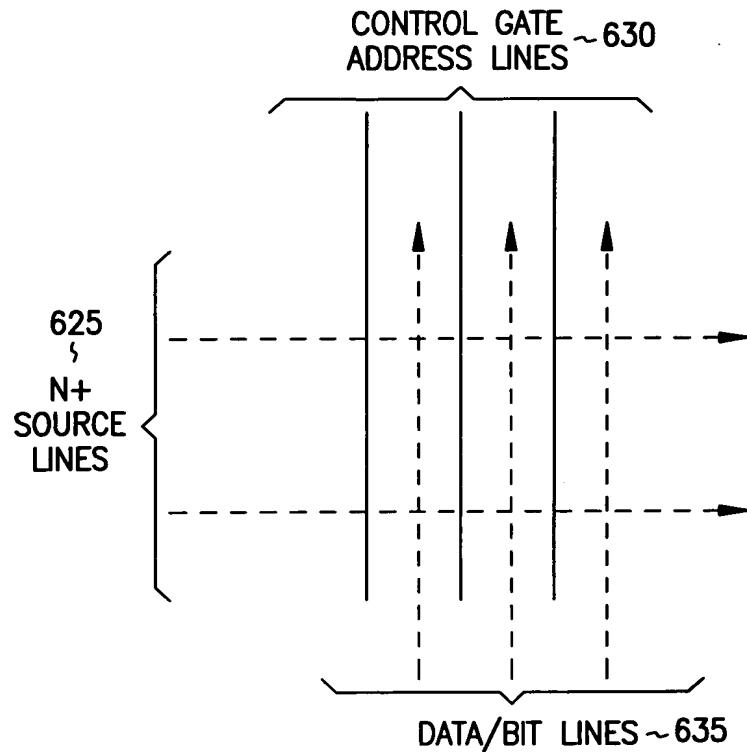


FIG. 6B

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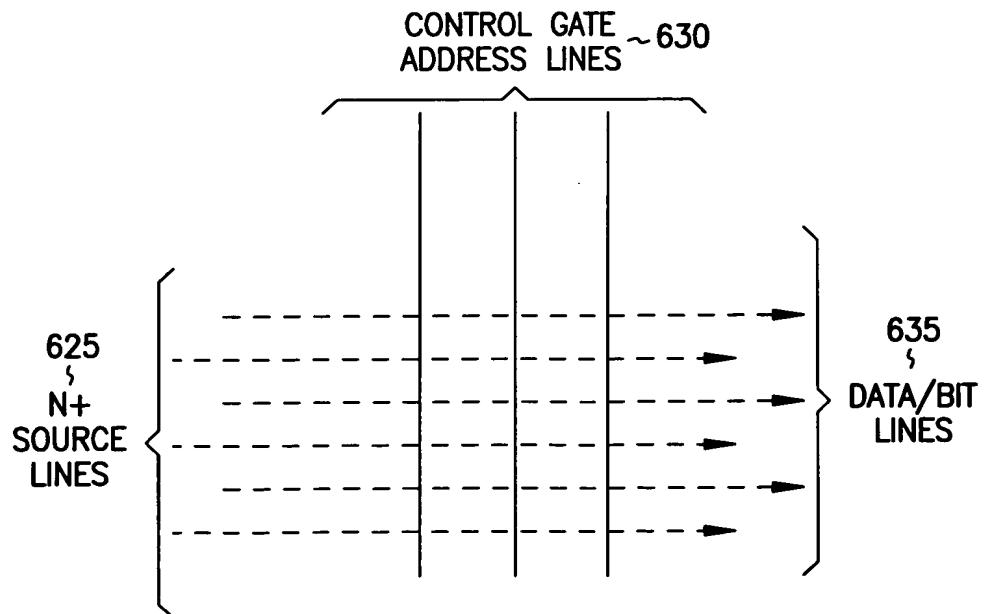


FIG. 6C

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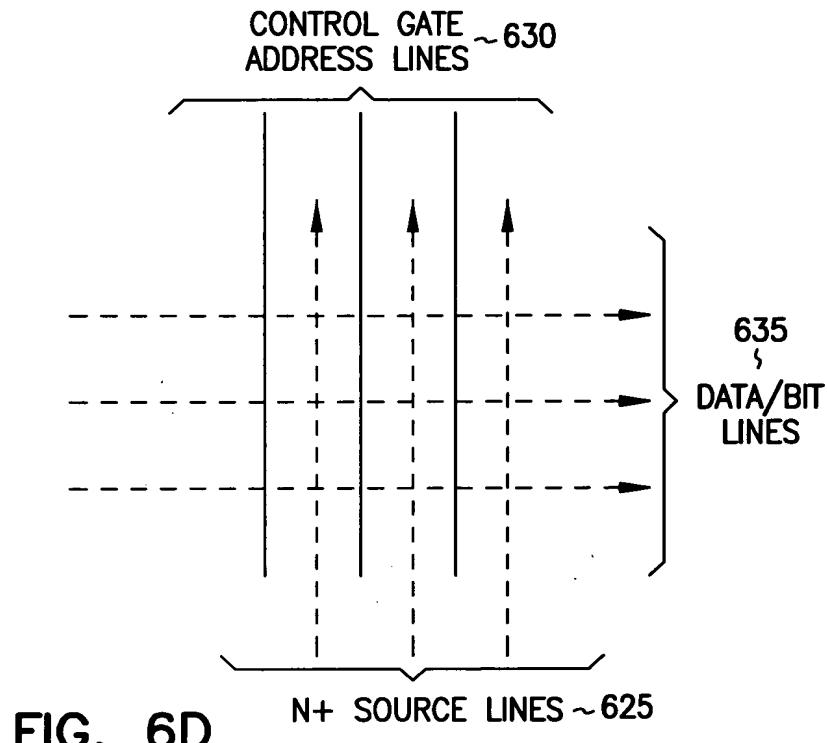


FIG. 6D

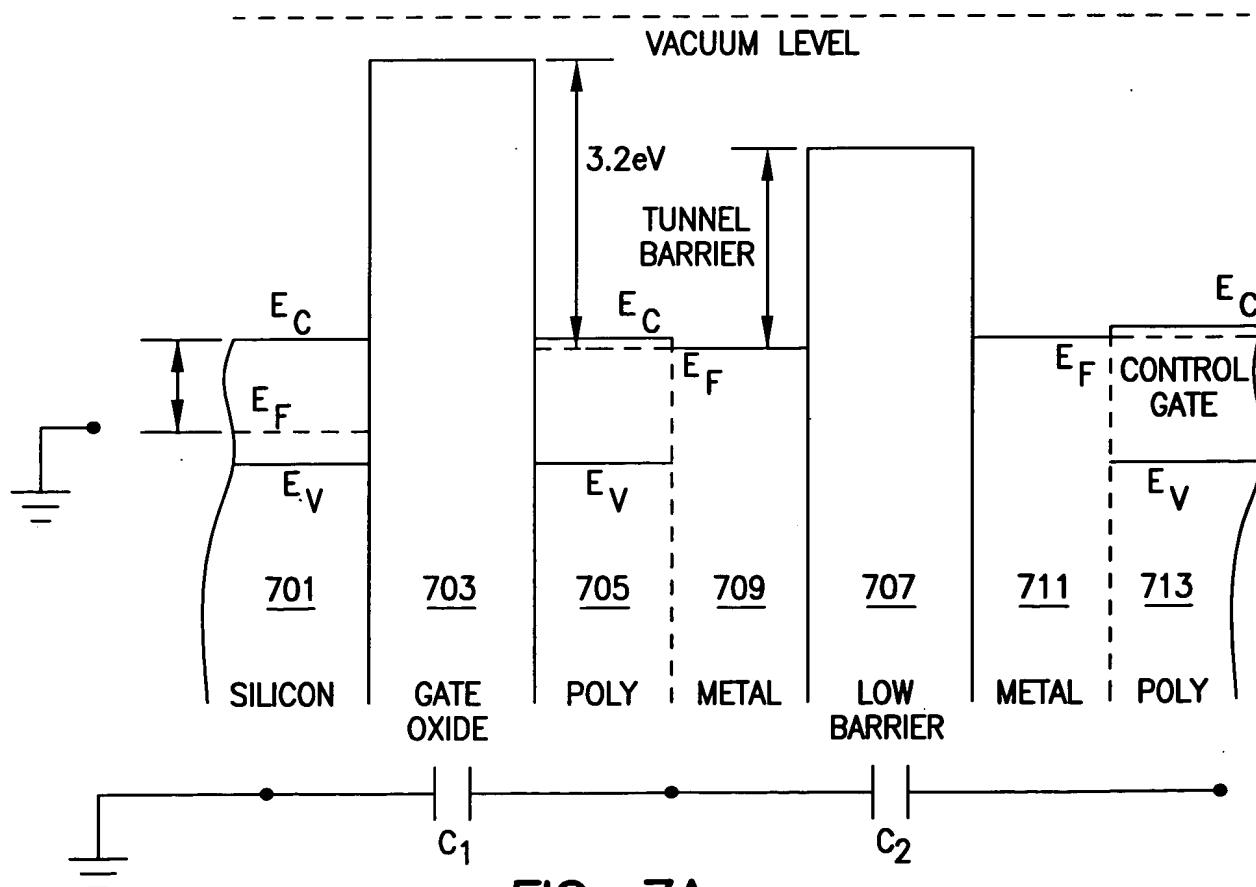
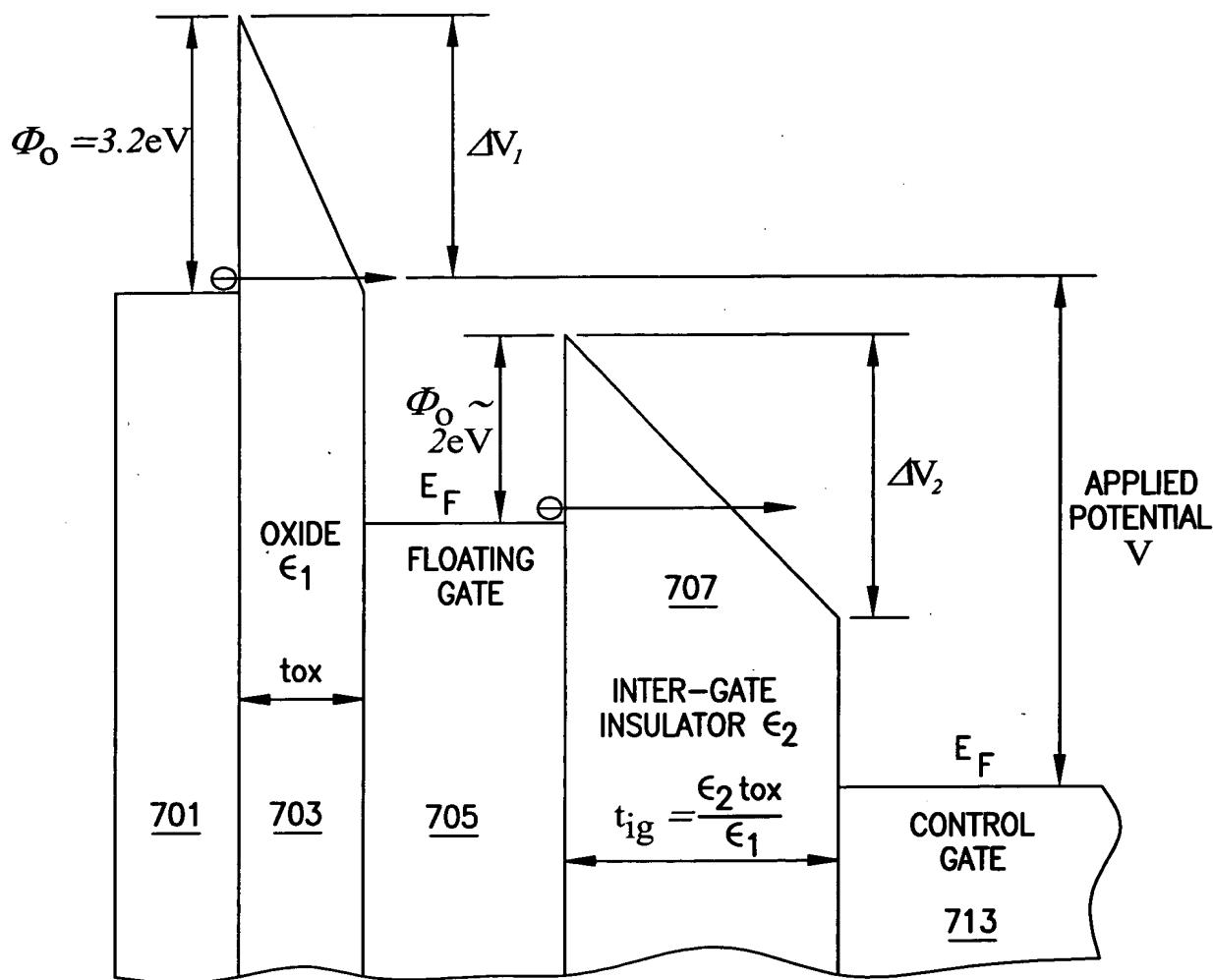


FIG. 7A

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$$J = \frac{q^2 E^2 - E_0/E}{4\pi h \Phi} e \quad E_0 = \frac{8\pi}{3} \sqrt{\frac{2m^*}{h}} \Phi^{3/2}$$

FIG. 7B

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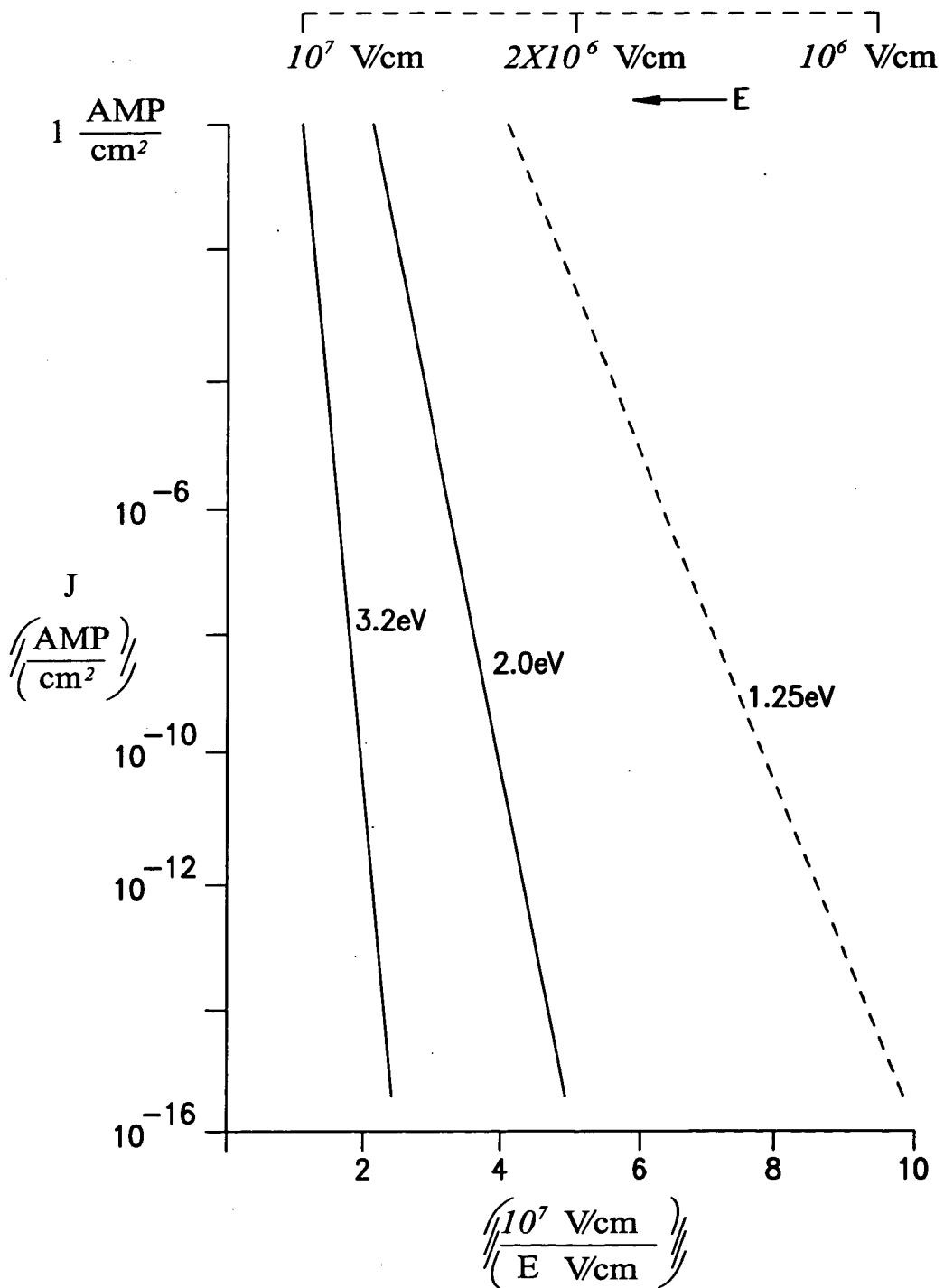


FIG. 7C

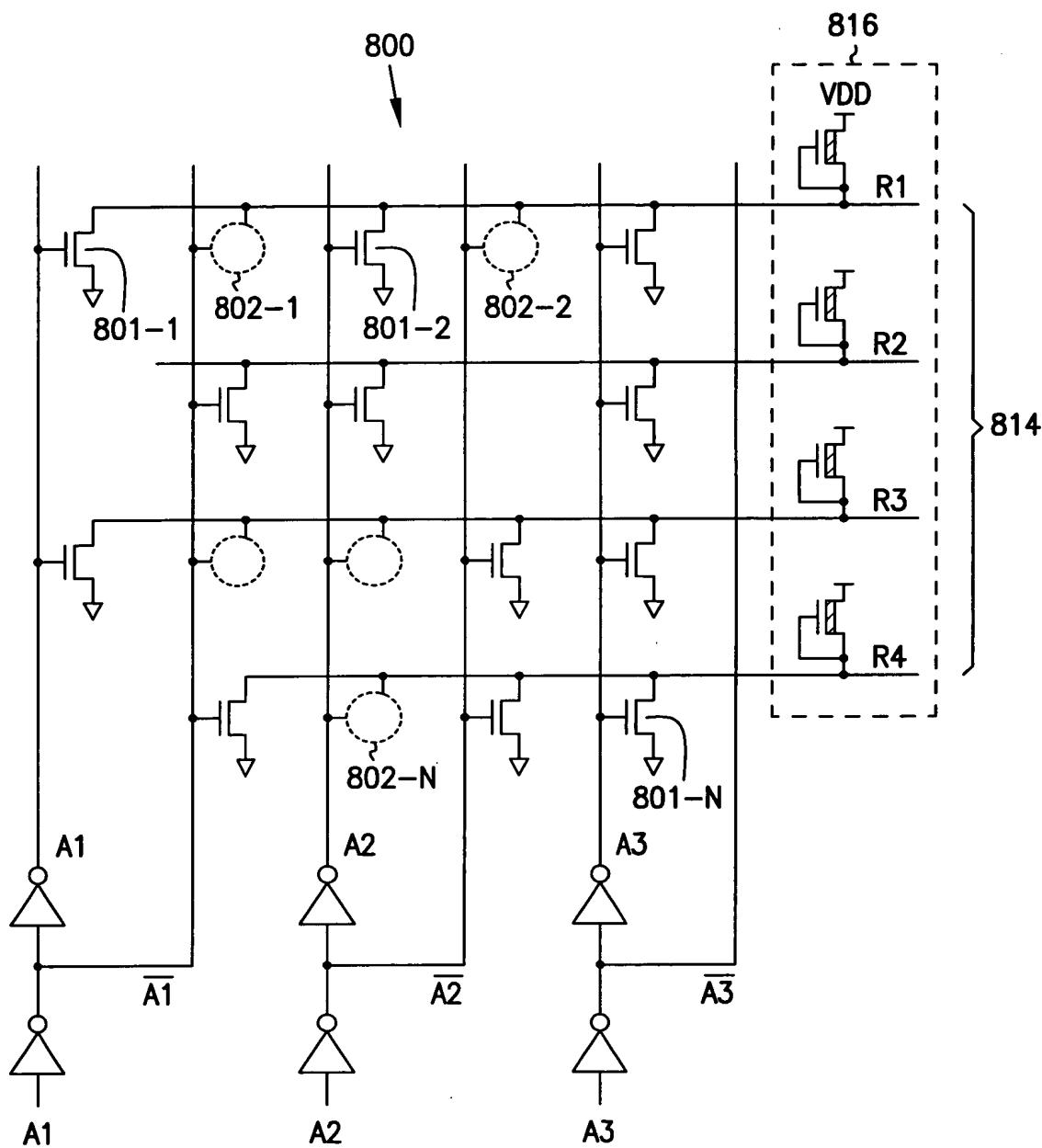


FIG. 8

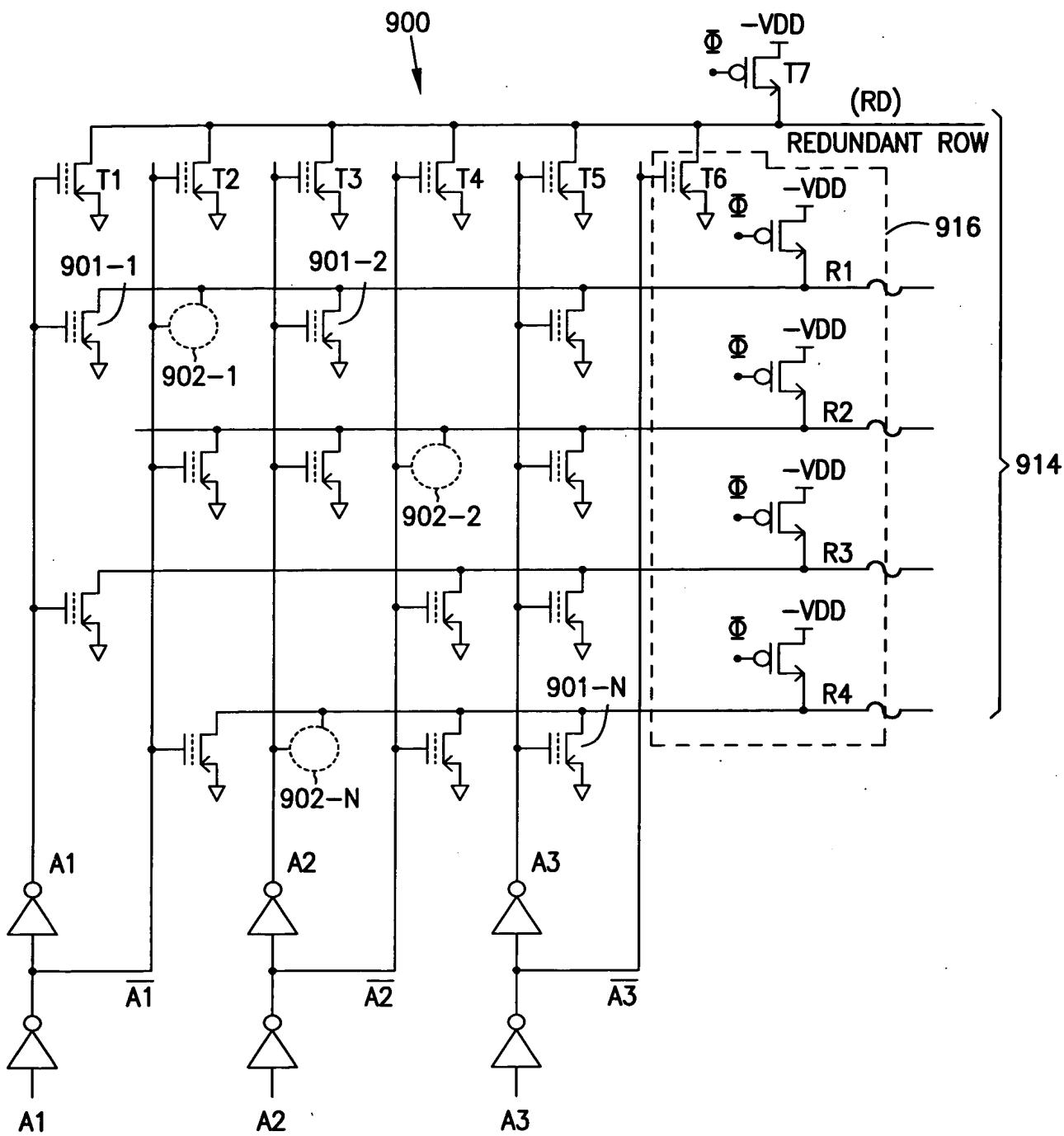


FIG. 9

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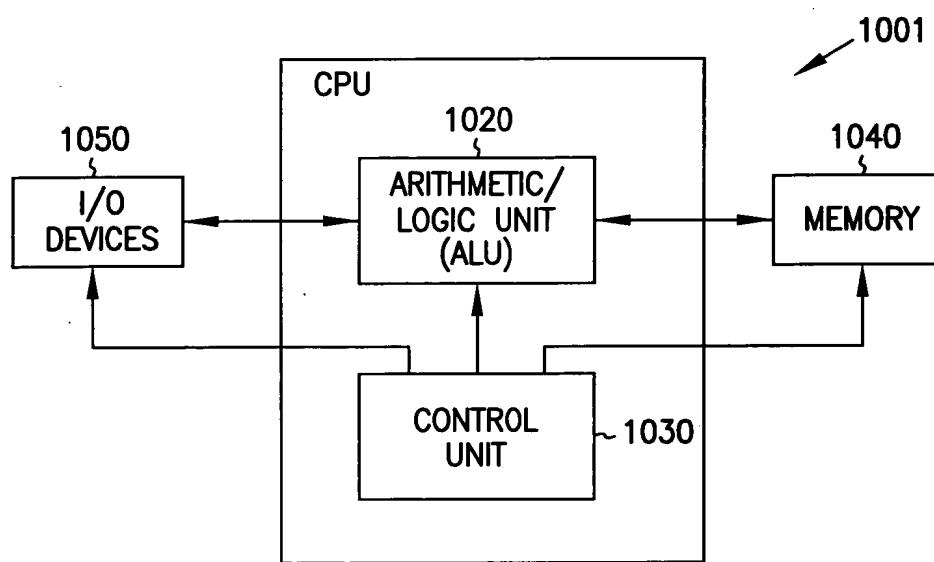


FIG. 10